

correction alternately in units of said predetermined capacity.

35. The error correction device of claim 12 further comprising:

two buffer memories each having a predetermined capacity

5 equivalent to one sector or one ECC block;

a buffer memory storage means for alternately storing in said two  
buffer memories, in accordance with error correction speed, continuous  
data of the predetermined capacity which are a target of error correction  
and have been read from a DVD or a CD-ROM; and

10 an accessed buffer memory switch means for switching between said  
two buffer memories in order to read or write data as a target of error  
correction alternately in units of said predetermined capacity.

36. The error correction device of claim 13 further comprising:

15 two buffer memories each having a predetermined capacity  
equivalent to one sector or one ECC block;

a buffer memory storage means for alternately storing in said two  
buffer memories, in accordance with error correction speed, continuous  
data of the predetermined capacity which are a target of error correction

20 and have been read from a DVD or a CD-ROM; and

an accessed buffer memory switch means for switching between said  
two buffer memories in order to read or write data as a target of error  
correction alternately in units of said predetermined capacity.

25 37. The error correction device of claim 14 further comprising:

two buffer memories each having a predetermined capacity equivalent to one sector or one ECC block;

a buffer memory storage means for alternately storing in said two buffer memories, in accordance with error correction speed, continuous  
5 data of the predetermined capacity which are a target of error correction and have been read from a DVD or a CD-ROM; and

an accessed buffer memory switch means for switching between said two buffer memories in order to read or write data as a target of error correction alternately in units of said predetermined capacity.

38. The error correction device of claim 15 further comprising:

two buffer memories each having a predetermined capacity equivalent to one sector or one ECC block;

a buffer memory storage means for alternately storing in said two  
15 buffer memories, in accordance with error correction speed, continuous data of the predetermined capacity which are a target of error correction and have been read from a DVD or a CD-ROM; and

an accessed buffer memory switch means for switching between said two buffer memories in order to read or write data as a target of error  
20 correction alternately in units of said predetermined capacity.

39. The error correction device of claim 16 further comprising:

two buffer memories each having a predetermined capacity equivalent to one sector or one ECC block;

a buffer memory storage means for alternately storing in said two  
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buffer memories, in accordance with error correction speed, continuous data of the predetermined capacity which are a target of error correction and have been read from a DVD or a CD-ROM; and

an accessed buffer memory switch means for switching between said

two buffer memories in order to read or write data as a target of error correction alternately in units of said predetermined capacity.

40. The error correction device of claim 17 further comprising:

two buffer memories each having a predetermined capacity equivalent to one sector or one ECC block;

a buffer memory storage means for alternately storing in said two buffer memories, in accordance with error correction speed, continuous data of the predetermined capacity which are a target of error correction and have been read from a DVD or a CD-ROM; and

an accessed buffer memory switch means for switching between said two buffer memories in order to read or write data as a target of error correction alternately in units of said predetermined capacity.

41. The error correction device of claim 18 further comprising:

two buffer memories each having a predetermined capacity equivalent to one sector or one ECC block;

a buffer memory storage means for alternately storing in said two buffer memories, in accordance with error correction speed, continuous data of the predetermined capacity which are a target of error correction

and have been read from a DVD or a CD-ROM; and